

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/820,026	04/08/2004	Hiroyuki Nagamori	HITA.0534	7585
7590 03/10/2006			EXAMINER	
Stanley P. Fisher			LE, DINH THANH	
Reed Smith LL	P			· · · · · · · · · · · · · · · · · · ·
Suite 1400			ART UNIT	PAPER NUMBER
3110 Fairview Park Drive			2816	
Falls Church, V	VA 22042-4503		DATE MAILED: 03/10/2006	5

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	10/820,026	NAGAMORI ET AL.	(m)
Notice of Allowability	Examiner	Art Unit	- (9)
(Supplementie)	DINH T. LE	2816	
The MAILING DATE of this communication apperature of the Communication appearance of the Co	(OR REMAINS) CLOSED or other appropriate comm IGHTS. This application is	in this application. If not included nunication will be mailed in due co	ourse. THIS
1. This communication is responsive to <u>11/16/05</u> .		<i>*</i>	
2. The allowed claim(s) is/are 1.2 and 4-13.			
3. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	e been received. e been received in Applicat cuments have been receiv of this communication to fi	ion No ed in this national stage applicatio	
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			TICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.		
(a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Revie	ew (PTO-948) attached	\
1) 🗌 hereto or 2) 🗍 to Paper No./Mail Date			v
(b) including changes required by the attached Examiner' Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			ack) of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MAT FOR THE DEPOSIT OF B	FERIAL must be submitted. No IOLOGICAL MATERIAL.	te the
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date	6. ☐ Interview Paper No Paper No 7. ☑ Examiner' 8. ☑ Examiner'	nformal Patent Application (PTO- Summary (PTO-413), ./Mail Date s Amendment/Comment s Statement of Reasons for Allowa	ŕ
* Note: To correct claimq.	9. ☐ Other	DINHT, LE	9

EXAMINER'S AMENDMENT (Supplemental)

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The following changes have been made in the subject of the application to put the claims in a condition for allowance.

Authorization for this examiner's amendment was given in a telephone interview with Attorney Joan Carlos Marquez on 3/3/06.

CLAIMS

In claim 9:

Line 8, replaced "input" with -output--.

REASONS FOR ALLOWANCE

The following is an Examiner's Statement of Reasons for Allowance:

The applicant's amendment filed 11/16/05 overcame the technical deficiencies and the prior art rejection. Claims 1-2 and 4-13 are allowed because the prior art of record does not suggest that "wherein a monitor voltage extraction point is provided between the portion connected to said first capacitor element and the portion connected to said second capacitor element of said microstrip line" as combined in claim 1, "the current detection circuit receives an output signal from a matching circuit and includes a first to fourth transistor" as combined in claim 8, and "the output power detection circuit receives a monitor voltage from an impedance matching via a capacitor" and "an output level control circuit" as combined in claim 12.

CONCLUSION

Any comments considered necessarily by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue

Art Unit: 2816

Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DINH T. LE whose telephone number is (571) 272-1745. The examiner can normally be reached on Monday-Friday (8AM-7PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY CALLAHAN can be reached at (571) 272-1740.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

3/3/06

DINHT.LE
PRIMARY EXAMINER